



2183

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :Gregory A. Overkamp Art Unit :2183
 et al. Examiner :Charles A. Harkness
Serial No.:09/675,816
Filed :September 28, 2000
Title :DIGITAL SIGNAL PROCESSOR WITH MULTIPLE INSTRUCTION
 DESTINATIONS FOR A PIPELINED SYSTEM

Mail Stop Appeal Brief - Patents
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

BRIEF ON APPEAL

Sir:

Applicant herewith files this brief on appeal, thereby perfecting the Notice of Appeal that was originally filed on July 20, 2004. The sections required by the rules follow:

(1) **Real Party in Interest**

The application is assigned of record to Intel Corp., and Analog Devices Inc., who are jointly the real party in interest.

(2) **Related Appeals and Interferences**

There are no known related appeals and/or interferences.

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

October 20, 2004

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